

10/576423

AP20 Rec'd PCT/PTO 19 APR 2006

Docket No.: 071971-0531

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of	: Customer Number: 20277
Hiroshi KIMURA	: Confirmation Number: Not yet assigned
Application No.: Not Yet Assigned	: Group Art Unit: Not Yet Assigned
Filed: April 19, 2006	: Examiner: Not yet assigned
For: OPTICAL RECEIVER PREAMPLIFIER	

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached form PTO-1449. It is respectfully requested that the references be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed with the application and no certification or fee is required.

A copy of the foreign search report is attached for the Examiner's information. Please note this is a PCT application in the entry of the National Phase in the U.S. and copies of the references cited were transmitted by WIPO and are believed to be in the file of the above identified application and readily available to the Examiner. Therefore it is believed that Applicants have met all requirements regarding duty of disclosure under 37

Not Yet Assigned

10/576423

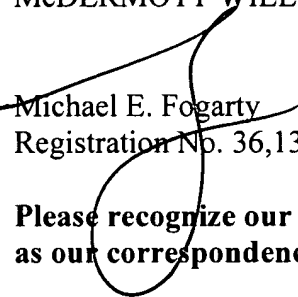
10/576423 10 APR 2006

CFR 1.56. Acknowledgement and consideration of these documents are respectfully requested.

A copy of the foreign search report is attached for the Examiner's information. Please note this is a PCT application in the entry of the National Phase in the U.S. Since the Search Report was from the U.S. JPO or EPO search authorities, copies of these references should have been supplied to the USPTO under the trilateral agreement and are believed to be in the file of the above identified application and readily available to the Examiner.

Respectfully submitted,

McDERMOTT WILL & EMERY LLP


Michael E. Fogarty
Registration No. 36,139

**Please recognize our Customer No. 20277
as our correspondence address.**

600 13th Street, N.W.
Washington, DC 20005-3096
Phone: 202.756.8000 MEF:aph
Facsimile: 202.756.8087
Date: April 19, 2006

RECEIVED BY APR 2006

SHEET 1 OF 1

INFORMATION DISCLOSURE CITATION IN AN APPLICATION (PTO-1449)				ATTY. DOCKET NO. 071971-0531		SERIAL NO. Not Yet Assigned 10/576423			
				APPLICANT Hiroshi KIMURA					
				FILING DATE April 19, 2006		GROUP Not Yet Assigned			
U.S. PATENT DOCUMENTS									
EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Code ² (if known)		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document		Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
		US	6,307,433 B1	10-23-2001	Ikeda				
		US	5,386,109	1-31-1995	Ikeuchi et al.				
		US							
		US							
		US							
		US							
		US							
		US							
		US							
		US							
		US							
		US							
		US							
		US							
		US							
		US							
		US							
		US							
FOREIGN PATENT DOCUMENTS									
EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Codes -Number 4 -Kind Codes (if known)		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document		Pages, Columns, Lines Where Relevant Figures Appear	Translation Yes No	
		JP 2001-217657		8-10-2001					
		JP 10-335948		12-18-1998					
		JP 9-232877		9-5-1997					
		JP 03-060208		3-15-1991	Hitachi Ltd.			English Abstract	
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)									
EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.							
		NISHIMURA et al., "A 1.25Gbit/s CMOS Burst-Mode Optical Transceiver for Ethernet PON System", 2004 Symposium on VLSI Circuits Digest of Technical Papers, pp. 414-417							
		YAMASHITA et al., "Novel Cell-AGC Technique for Burst-Mode CMOS Preamplifier With Wide Dynamic Range and High Sensitivity for ATM-PON System", IEEE Journal of Solid-State Circuits, Vol. 37, No. 7, July 2002, pp. 881-886							
EXAMINER					DATE CONSIDERED				

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.